

FIG. 1a

FIG. 1b

N-ch MOSFET LAYOUT

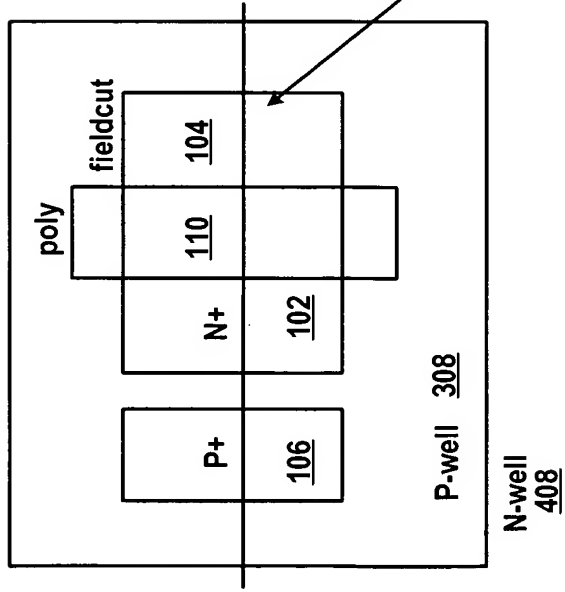


FIG. 2a

N-ch MOSFET CROSS SECTION

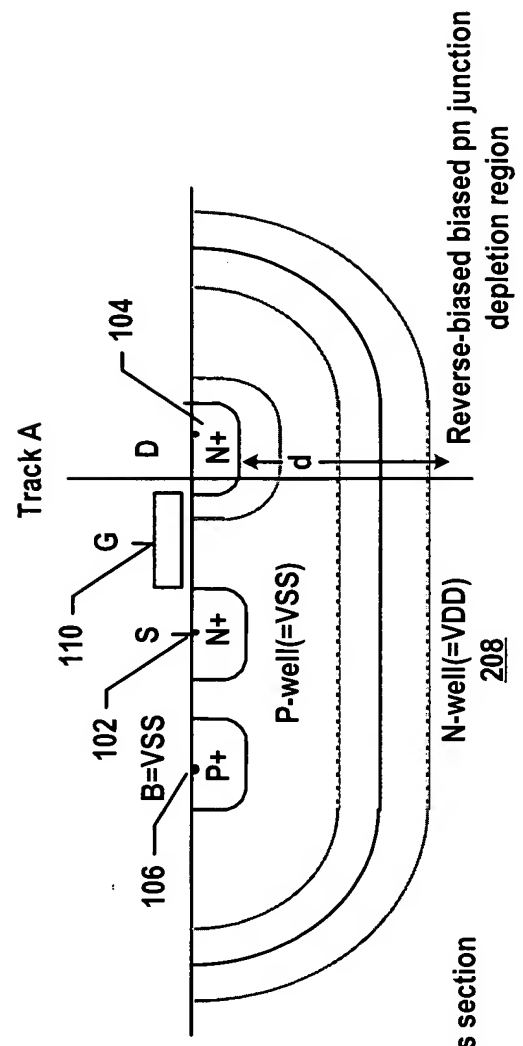
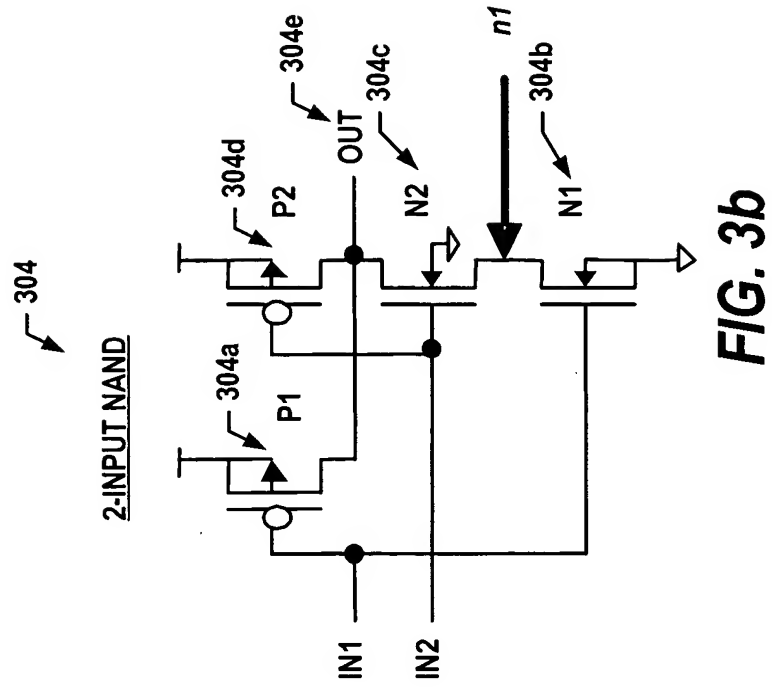
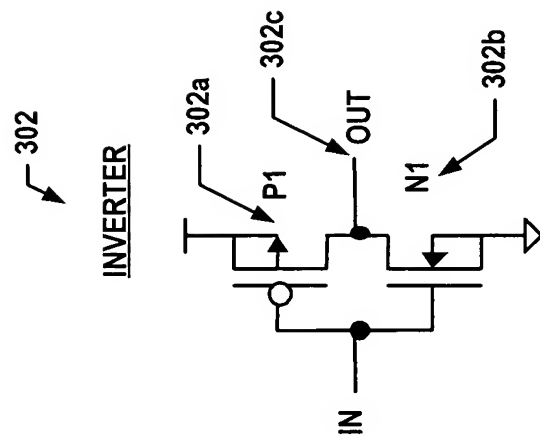


FIG. 2b



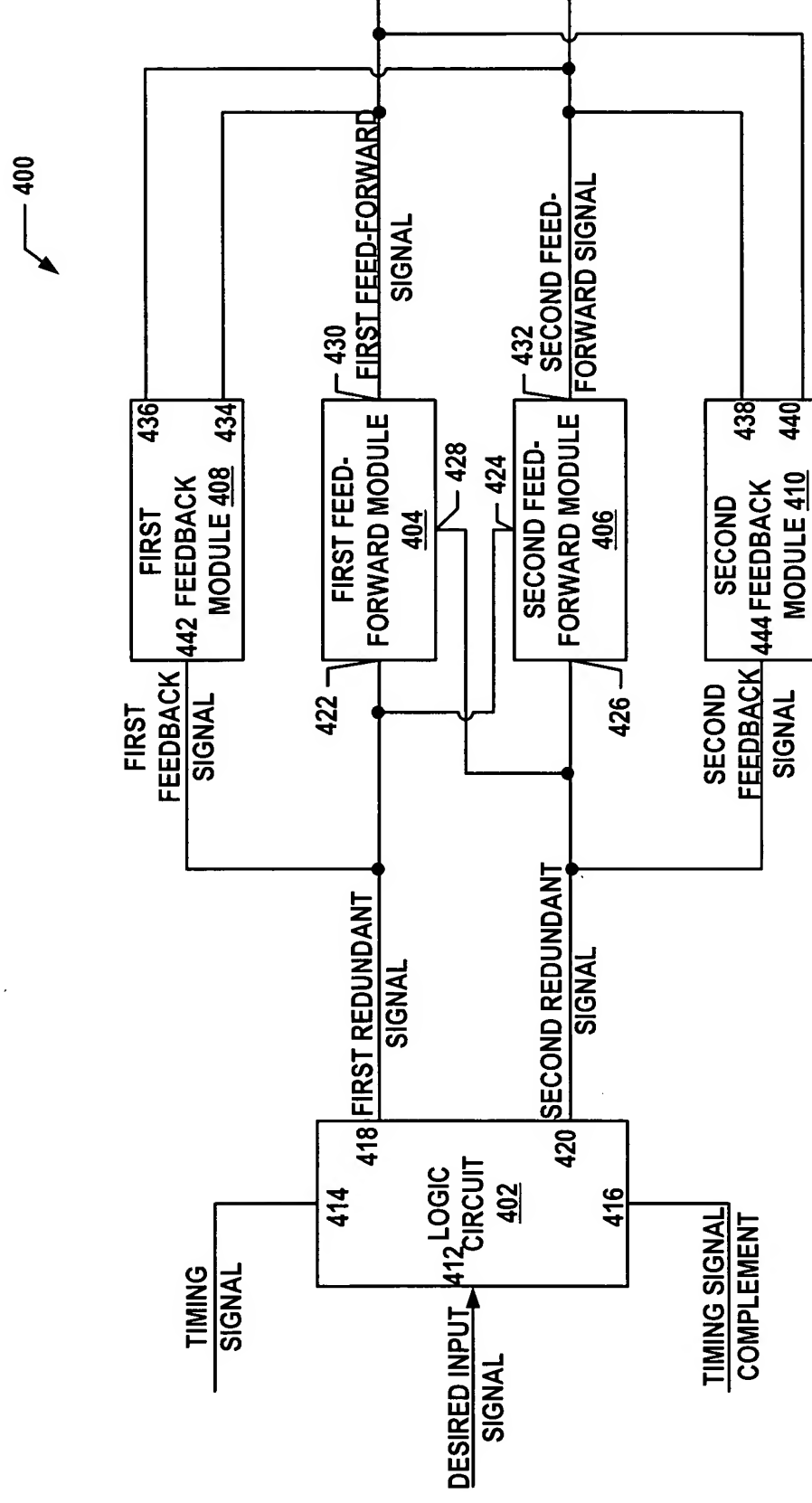


FIG. 4

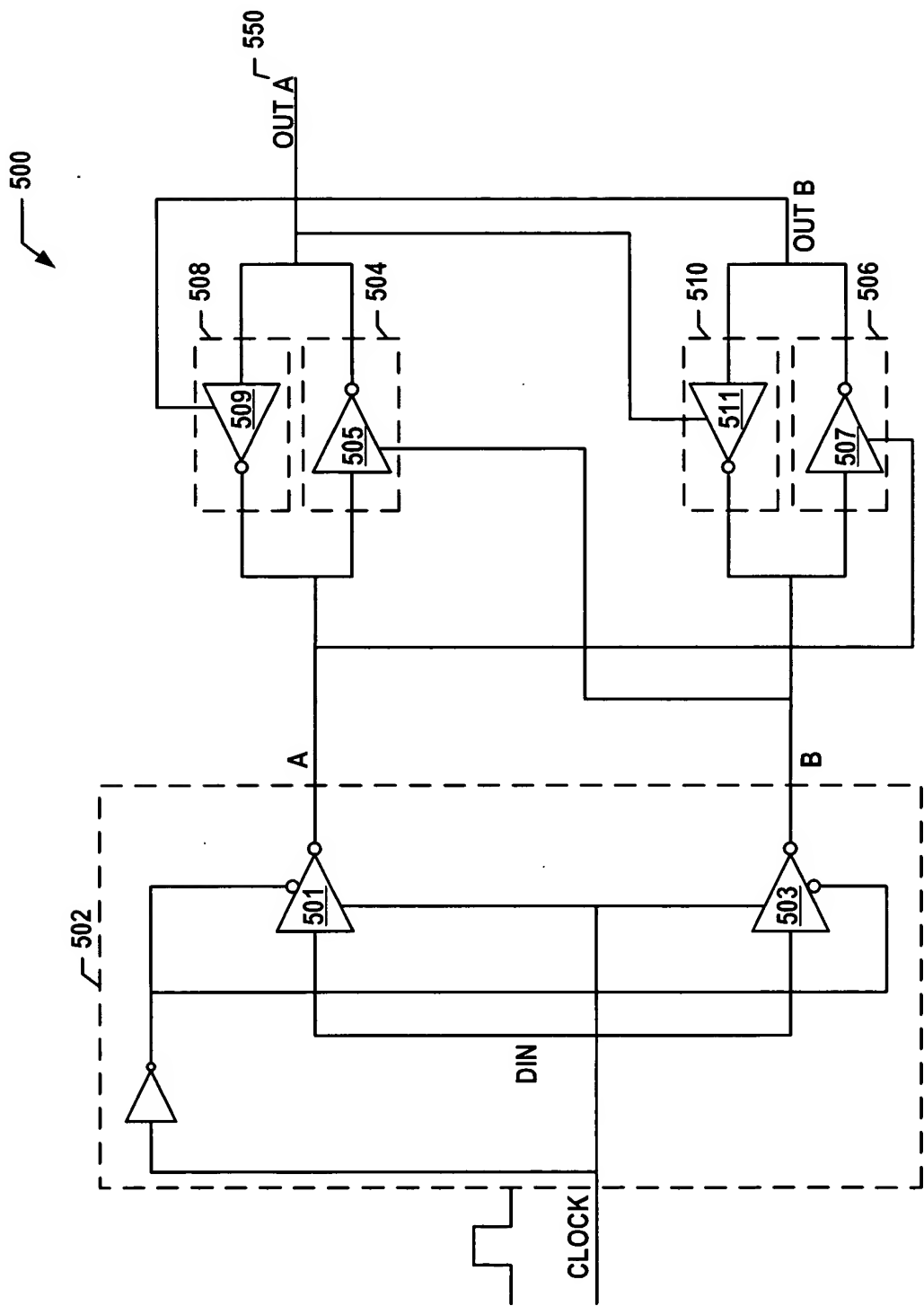


FIG. 5

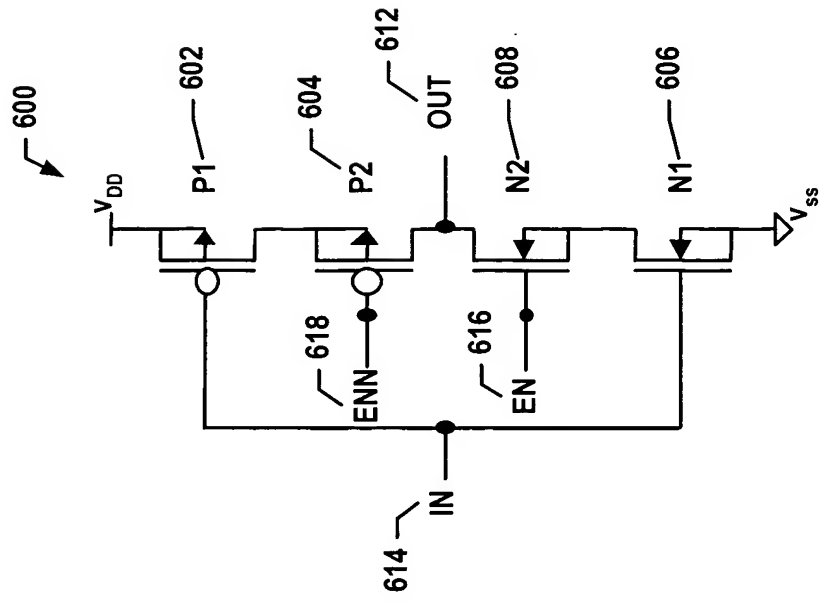


FIG. 6

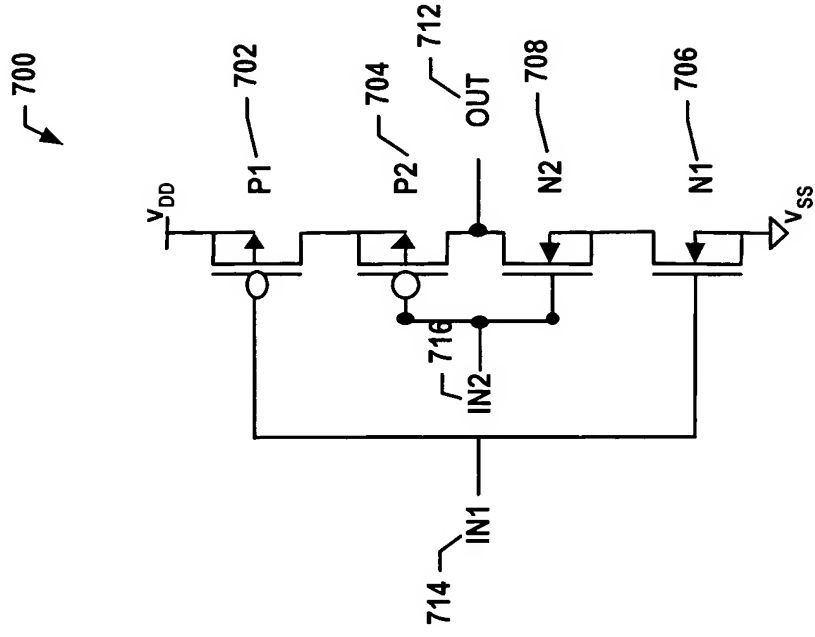


FIG. 7

800

N-ch MOSFET LAYOUT

N-ch MOSFET CROSS SECTION

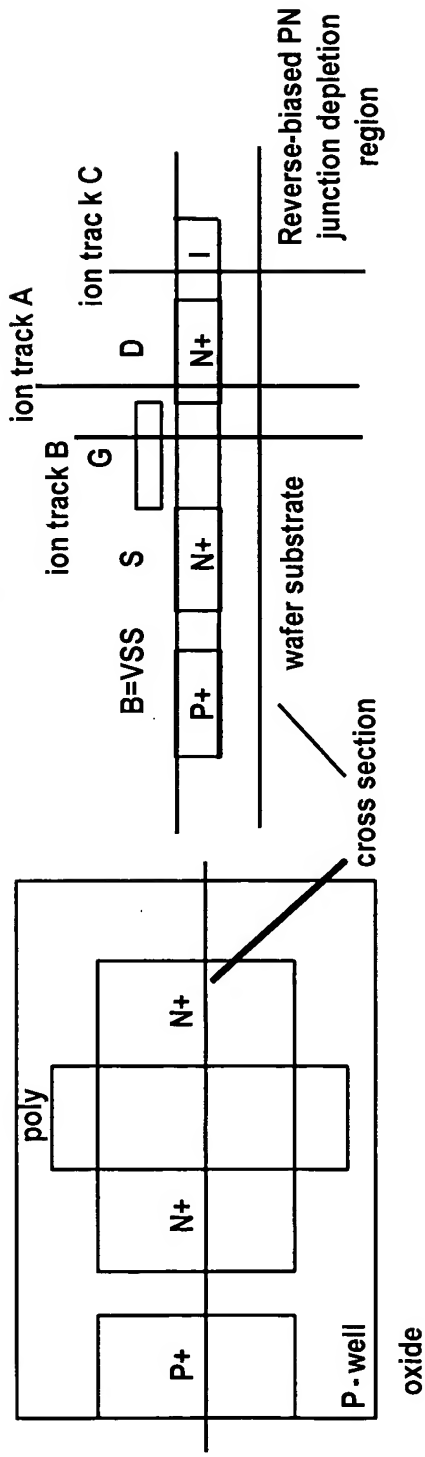
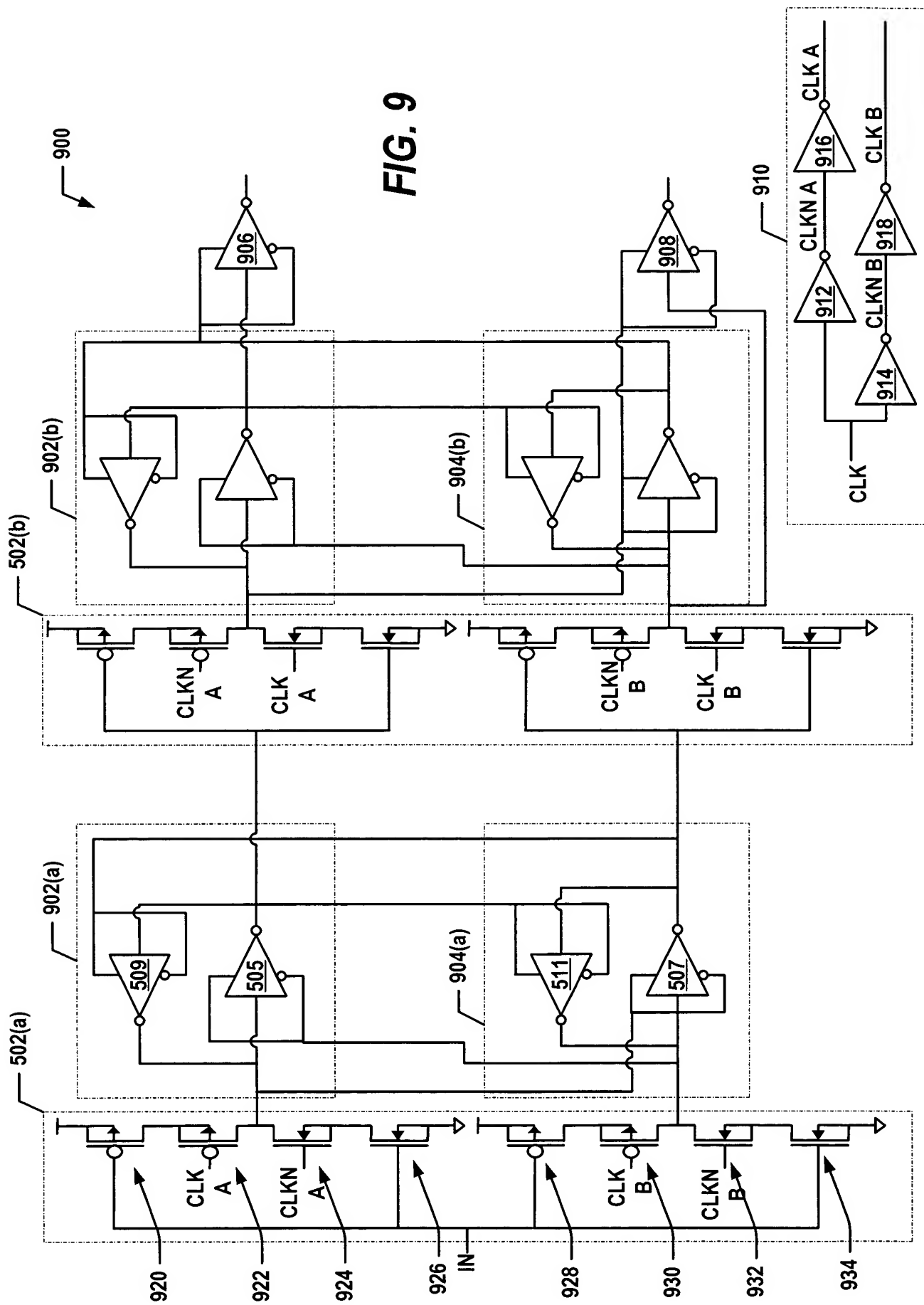


FIG. 8a

FIG. 8b



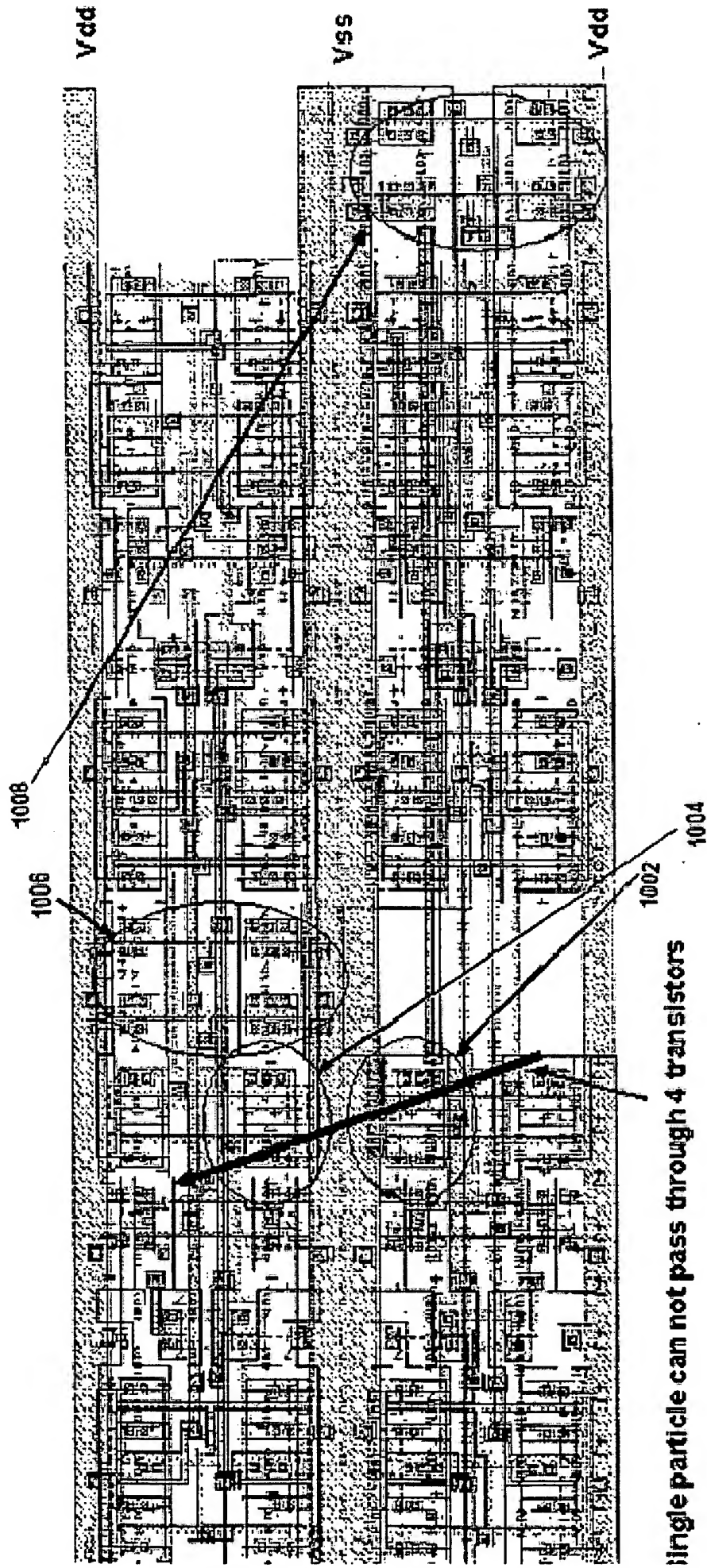


FIG. 10

BEST AVAILABLE COPY